

Serial No. 10/002,741

Page 2 of 8

**IN THE CLAIMS****1. (currently amended) A redundant changeover apparatus comprising:**

a changeover unit to change over from one to another of two input signals which are mutually asynchronous in phase,

an extracting unit to extract a clock from an output signal of the changeover unit,

a PLL circuit to receive the clock from the extracting unit and to output an output for inputting the extracted clock,

a clock changing unit to provide~~receive~~ the output signal with a ~~clock changed to an~~the output clock of the PLL circuit, and

a framing unit to frame output data of the clock changing unit with the output clock.

**2. (currently amended) A redundant changeover apparatus comprising:**

two extracting units to extract data and a clock respectively of two input signals which are mutually asynchronous in phase,

a first and a second reference clock changing unit to change the respective data with a reference clock inputted externally,

a first changeover unit to change over from one to another of data respectively outputted from the first and the second reference clock changing unit,

a second changeover unit to change over from one to another of clocks extracted by the extracting units,

a PLL circuit to receive the clock from the second changeover unit and to output an output for inputting a clock outputted by the second changeover unit, and

84216797\_1

Serial No. 10/002,741  
Page 3 of 8

a clock changing unit to change output data of the first changeover unit from a clock before the changeover to an output clock of the PLL circuit.

3. (previously presented) The redundant changeover apparatus as claimed in claim 2 wherein the reference clock comprises an in-house clock or a free-running clock .

4. (previously presented) The redundant changeover apparatus as claimed in claim 2 wherein the clock extracting unit extracts a clock from a wavelength division multiplexing device.

5. (previously presented) The redundant changeover apparatus as claimed in claim 1 wherein the input signals comprise a working input signal and a protection input signal from a wavelength division multiplexing device forming a ring network.

6. (previously presented) The redundant changeover apparatus as claimed in claim 1 wherein the input signals comprise a working input signal and a protection input signal from an arbitrary transmission device of a client.

7. (original) The redundant changeover apparatus as claimed in claim 1 wherein the changeover unit comprises an optical switch.

8. (original) The redundant changeover apparatus as claimed in claim 2 wherein the first changeover unit comprises an optical switch and the second changeover unit comprises an electric switch.

84216797\_1

Serial No. 10/002,741  
Page 4 of 8

**9. (previously presented)** The redundant changeover apparatus as claimed in claim 1 wherein the clock changing unit comprises the PLL circuit.

**10. (currently amended)** A node device comprising:

redundant changeover apparatuses, provided in duplicate for same transmission lines of a working system and a protection system,

each redundant changeover apparatus comprises a changeover unit to change over from one to another of two input signals which are mutually asynchronous in phase, an extracting unit to extract a clock from an output signal of the changeover unit, a PLL circuit to receive the clock from the extracting unit and to output an output for inputting the extracted clock, a clock changing unit to ~~provide~~receive the output signal with ~~a clock changed to an~~ the output clock of the PLL circuit, and a framing unit to frame output data of the clock changing unit with the output clock, and generates outputs of the clock changing unit of the working system and the protection system.

**11. (original)** The node device as claimed in claim 10 wherein the changeover unit is commonly provided for each redundant changeover apparatus.

**12. (previously presented)** The redundant changeover apparatus as claimed in claim 2 wherein the input signals comprise a working input signal and a protection input signal from a wavelength division multiplexing device forming a ring network.

84216797\_1